	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	2416	(polish$3 or planariz$4) same (radial$2 or annular) same (wafer or semiconductor or substrate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/27 11:50			
2	BRS	L2	1105	(polish$3 or planariz$4) with (radial$2 or annular) with (wafer or semiconductor or substrate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/27 11:51			
3	BRS	L3	156	2 same thickness	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/27 11:53			
4	BRS	L4	60	3 and @pd<="20010831"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/27 11:54			
5	BRS	L5	1	2 same (thickness and model)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/27 11:53			
6	BRS	L6	1	(2 same thickness) same model	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/27 11:54			
7	BRS	L7	22	(2 same thickness) and model	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/27 11:54			
8	BRS	L8	4	7 and @pd<="20010831"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/27 11:54			